



Disaster Prevention Optimizations, Insights, and Best Practices

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AGENDA

- **1** Introduction
- 2 Goals
- **3** Proactively Identifying Impending Issues
- 4 Recommendations and Optimizations
- **5** Preventing Data Loss and Downtime



Disaster Prevention How to prepare for a visit from Mayhem

Disaster Prevention How to prepare for a visit from Mayhem



Mayhem is everywhere

Problems vs Disasters

Problems

- May occur on a regular basis and are often outside our control
 - Hardware failure
 - Accidental deletion of files
 - Communication device failures

Disasters

- Occur when we're not prepared to handle a problem
 - System downtime
 - Data loss
 - UIS instability

Disaster Prevention

- Goals
 - Awareness
 - Not a deep dive
- Other resources
 - White paper
 - Call CygNet Support
 - 2017 Support project

Proactively Identifying Impending Problems – Before they Turn into Critical Issues

Catching Mayhem at the door

The Presenting Problem

Presenting Problem

- The initial <u>symptom</u> which signals that an issue exists
 - Not the root issue
 - Detectable and Correctable

How do you proactively identify the root issue?



CygNet Service Monitor (SVCMON)

What is SVCMON?

- -CygNet Current Value Service (CVS)
- -Gathers statistics
 - Host
 - Site
 - Service



CygNet Service Monitor (SVCMON)

- Monitor real-time statistics
 - Define acceptable ranges
 - Set up alarms and notifications
- Trend system performance
 - Analysis
 - Performance degradation
 - Troubleshooting

- Items to monitor?
 - All of them
- Data retention?
 - At least a year

- Alarm
 - Abnormalities



- Alarm
 - Abnormalities
 - Limits and thresholds
 - Priorities
 - Categorization
- Notifications
 - Critical alarms

General History Point referen	ce Commer	nts Applic	ation Analog					
Point scheme: CygNet Standard (0) Point data type: Analog Output (AO)			Alam category for point:					
Suppress alarms: No Suppression Enabled								
Value range								
Minimum: 0			Alarm deadband: 0					
Maximum: 0			Unit conversion					
Enable CVS Calculation	Calc. Value 1	Calc. Value 2	Alarm Priority	Report To CAS	Report To GNS	GNS ID	Delay Reporting	
× Low Alarm	Setpoint 10		0-99 (Category) 99 (CRITICAL)	×	×	ALERT		
× Low Warning	Setpoint 50		0-99 (Category) 74 (HIGH)	×				
High Warning	Setpoint		0-99 (Category) 0 (LOW)					
High Alarm	Setpoint		0-99 (Category)					

NOTE Service



- Considerations
 - Dedicated SVCMON services
 - VHS
 - CAS
 - Dedicated SVCMON site

Event Logging Service (ELS)

- All services
- Overview
- Review daily
- Log writes to ELS (SVMLOGSWRT)

Service Log Files

- Service specific
- Detailed view
- Review as needed
- Log writes to file (SVMLOGFWRT)

- Clean up
 - Quickly spot abnormal behavior
 - Easily identify relevant content

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4/1/2017 10:33:45.899 12788	EXCEPTION(V): SWANK_C:TF F TankData(0):	RegNum: 20.3.30, size of 0 bytes is invalid (-2147483638), RegisterMag.cpp line 264 (class CRegisterFromDeviceMag). Handled: RegisterMag.cpp line 270 VaiException.cpp 628 nLOG_EXCEPTIONS
4/1/2017 10:33:45.899 12788	EXCEPTION(V): SWANK C:TF F TankData(0):	RegNum: 20.3.31, size of 0 bytes is invalid (-2147483638), RegisterMag.cpp line 264 (class CRegisterFromDeviceMag), Handled: RegisterMag.cpp line 270 VaiException.cpp 628 nLOG EXCEPTIONS
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4/1/2017 10:33:45.899 12788	EXCEPTION(V): SWANK_C:TF F TankData(0):	RegNum: 20.12.4, size of 0 bytes is invalid (-2147483638), RegisterMag.opp line 264 (class CRegisterFromDeviceMag). Handled: RegisterMag.opp line 270 VaiException.opp 628 nLOG_EXCEPTIONS
4/1/2017 10:33:45.899 12788	EXCEPTION(V): SWANK_C:TF F TankData(0):	RegNum: 20.12.6, size of 0 bytes is invalid (-2147483638), RegisterMag.cpp line 264 (class CRegisterFromDeviceMag). Handled: RegisterMag.cpp line 270 VaiException.cpp 628 nLOG_EXCEPTIONS
4/1/2017 10:33:45.915 12788	EXCEPTION (V) : SWANK C:TF F TankData(0) :	ReqNum: 20.12.8, size of 0 bytes is invalid (-2147483638), RegisterMag.cpp line 264 (class CRegisterFromDeviceMag). Handled: RegisterMag.cpp line 270 VsiException.cpp 628 nLOG EXCEPTIONS
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4/1/2017 10:33:53.415 13684	EXCEPTION(V): THOM_F:TF F TankDataW(0):	Deid: DIO3 expected 4 bytes, reg size is 1 bytes, Skipping Deid (-2147483638), RegisterMag.cpp line 658 (class CRegisterFromDeviceMag). Handled: RegisterMag.cpp line 270 VsiException.cpp 628 nLOG_EXCEPTIONS
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4/1/2017 10:33:53.415 13684	EXCEPTION(V): THOM_F:TF F TankDataW(0):	Deid: DI12 expected 4 bytes, reg size is 1 bytes, Skipping Deid (-2147483638), RegisterMag.cpp line 658 (class CRegisterFromDeviceMag). Handled: RegisterMag.cpp line 270 VaiException.cpp 628 nLOG_EXCEPTIONS
4/1/2017 10:33:53.431 13684	EXCEPTION(V): THOM F:TF F TankDataW(0):	Daid: TakComLoss expected 4 bytes, reg size is 2 bytes, Skipping Daid (-2147483638), RegisterMag.cop line 658 (class CRegisterFromDaviceMag), Handlad: RegisterMag.cop line 270 VaiException.cop 628 nLOG EXCEPTIONS
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4/1/2017 10:34:25.165 13684 4/1/2017 10:34:25.181 13684 4/1/2017 10:34:25.181 13684 4/1/2017 10:34:25.181 13684 4/1/2017 10:34:25.181 13684 4/1/2017 10:34:25.181 13684 4/1/2017 10:34:25.181 13684	EXCEPTION('): MILLER_Y:TF F TARKGIAW() EXCEPTION('): MILLER_Y:TF F TARKGIAW()	1) Daid: D10 sepected 4 bytes, esq iie is 1 bytes, Skipping Daid (-21474858), RegisterNay, opp line 550 (class CRegisterFomDericeMay), Handled: RegisterMay, opp line 270 Vilkoeption.cpp 628 nLOC_EXCEPTIONS)) Daid: D104 sepected 4 bytes, esq iie is 1 bytes, Skipping Daid (-21474858), RegisterMay, opp line 658 (class CRegisterFomDericeMay), Handled: RegisterMay, opp line 270 Vilkoeption.cpp 628 nLOC_EXCEPTIONS)) Daid: D104 sepected 4 bytes, esq iie is 1 bytes, Skipping Daid (-21474858), RegisterMay, opp line 658 (class CRegisterFomDericeMay), Handled: RegisterMay, opp line 270 Vilkoeption.cpp 628 nLOC_EXCEPTIONS): Daid: D104 sepected 4 bytes, esq iie is 1 bytes, Skipping Daid (-214748583), RegisterMay, opp line 658 (class CRegisterFomDericeMay), Handled: RegisterMay, opp line 270 Vilkoeption.cpp 628 nLOC_EXCEPTIONS): Daid: D105 sepected 4 bytes, esq iie is 1 bytes, Skipping Daid (-214748583), RegisterMay, opp line 658 (class CRegisterFomDericeMay), Handled: RegisterMay, opp line 270 Vilkoeption.cpp 628 nLOC_EXCEPTIONS): Daid: D105 sepected 4 bytes, esq iie is 1 bytes, Skipping Daid (-214748583), RegisterMay, opp line 658 (class CRegisterFomDericeMay), Handled: RegisterMay, opp line 270 Vilkoeption.cpp 628 nLOC_EXCEPTIONS): Daid: D105 sepected 4 bytes, esq iie is 1 bytes, Skipping Daid (-214748583), RegisterMay, opp line 658 (class CRegisterFomDericeMay), Handled: RegisterMay, opp line 270 Vilkoeption.cpp 628 nLOC_EXCEPTIONS): Daid: D105 sepected 4 bytes, esq iie is 1 bytes, Skipping Daid (-214748583), RegisterMay, opp line 658 (class CRegisterFomDericeMay), Handled: RegisterMay, opp line 270 Vilkoeption.cpp 628 nLOC_EXCEPTIONS): Daid: D105 sepected 4 bytes, esq iie is 1 bytes, Skipping Daid (-214748583), RegisterMay, opp line 658 (class CRegisterFomDericeMay), Randled: RegisterMay, opp line 270 Vilkoeption.cpp 628 nLOC_EXCEPTIONS): Daid: D105 sepected 4 bytes, esq iie is 1 bytes, Skipping Daid (-214748583), RegisterMay, opp line 658 (class CRegiste
4/1/2017 10:34:25.165 13684 4/1/2017 10:34:25.181 13684 4/1/2017 10:34:25.181 13684 4/1/2017 10:34:25.181 13684 4/1/2017 10:34:25.181 13684 4/1/2017 10:34:25.181 13684 4/1/2017 10:34:25.181 13684	I EXCEPTION('): NILLER_'TF F TANKGLAW() EXCEPTION('): NILLER_'TF F TANKGLAW()	1) Daid: DD2 expected 4 bytes, reg lies is 1 bytes, Skipping Daid (=214748583), RegisterMeg.cpp line 658 (class CRegisterFomDericaMeg), Handled: RegisterMeg.cpp line 270 Vilkeception.cpp 628 nLO2_EXCEPTIONS): Daid: DD2 expected 4 bytes, reg lies is 1 bytes, Skipping Daid (=214748583), RegisterMeg.cpp line 1658 (class CRegisterFomDericaMeg), Handled: RegisterMeg.cpp line 270 Vilkeception.cpp 628 nLO2_EXCEPTIONS): Daid: DD2 expected 4 bytes, reg lies is 1 bytes, Skipping Daid (=214748583), RegisterMeg.cpp line 658 (class CRegisterFomDericaMeg), Handled: RegisterMeg.cpp line 270 Vilkeception.cpp 628 nLO2_EXCEPTIONS): Daid: DD2 expected 4 bytes, reg lies is 1 bytes, Skipping Daid (=214748583), RegisterMeg.cpp line 658 (class CRegisterFomDericaMeg), Handled: RegisterMeg.cpp line 270 Vilkeception.cpp 628 nLO2_EXCEPTIONS): Daid: DD3 expected 4 bytes, reg lies is 1 bytes, Skipping Daid (=214748583), RegisterMeg.cpp line 658 (class CRegisterFomDericaMeg), Handled: RegisterMeg.cpp line 270 Vilkeception.cpp 628 nLO2_EXCEPTIONS): Daid: DD3 expected 4 bytes, reg lies is 1 bytes, Skipping Daid (=214748583), RegisterMeg.cpp line 658 (class CRegisterFomDericaMeg), Handled: RegisterMeg.cpp line 270 Vilkeception.cpp 628 nLO2_EXCEPTIONS): Daid: DD3 expected 4 bytes, reg lies is 1 bytes, Skipping Daid (=214748583), RegisterMeg.cpp line 658 (class CRegisterFomDericaMeg), Handled: RegisterMeg.cpp line 270 Vilkeception.cpp 628 nLO2_EXCEPTIONS): Daid: DD3 expected 4 bytes, reg site is 1 bytes, Skipping Daid (=214748583), RegisterMeg.cpp line 658 (class CRegisterFomDericaMeg), Handled: RegisterMeg.cpp line 270 Vilkeception.cpp 628 nLO2_EXCEPTIONS): Daid: DD3 expected 4 bytes, reg site is 1 bytes, Skipping Daid (=214748583), RegisterMeg.cpp line 658 (class CRegisterFomDericaMeg), Handled: RegisterMeg.cpp line 270 Vilkeception.cpp 628 nLO2_EXCEPTIONS): Daid: DD3 expected 4 bytes, reg site is 1 bytes, Skipping Daid (=214748583), RegisterMeg.cpp line 658 (class CRegisterFomD
4/1/2017 10:34:25.165 13684 4/1/2017 10:34:25.161 13684	I EXCEPTION (") HILLER_Y:TF F TankbardW() I EXCEPTION (") HILLER_Y:TF F TankbardW()	1) Daid: D10 sepactad 4 bytes, esq iie ii bytes, Skipping Daid (-21474858), RagisterMag.cpp line 58 (class CRegisterFomDeviceMag), Handled: RagisterMag.cpp line 270 Vilkosption.cpp 628 nLOE_TXCHFTIONS 1) Daid: D104 sepactad 4 bytes, esq iie ii bytes, Skipping Daid (-21474858), RagisterMag.cpp line 658 (class CRegisterFomDeviceMag), Handled: RagisterMag.cpp line 270 Vilkosption.cpp 628 nLOE_TXCHFTIONS 1) Daid: D104 sepactad 4 bytes, esq iie ii bytes, Skipping Daid (-21474858), RagisterMag.cpp line 658 (class CRegisterFomDeviceMag), Handled: RagisterMag.cpp line 270 Vilkosption.cpp 628 nLOE_TXCHFTIONS 1) Daid: D105 sepactad 4 bytes, esq iie ii bytes, Skipping Daid (-214748583), RagisterMag.cpp line 658 (class CRegisterFomDeviceMag), Handled: RagisterMag.cpp line 270 Vilkosption.cpp 628 nLOE_TXCHFTIONS 1) Daid: D105 sepactad 4 bytes, esq iie ii bytes, Skipping Daid (-214748583), RagisterMag.cpp line 658 (class CRegisterFomDeviceMag), Handled: RagisterMag.cpp line 270 Vilkosption.cpp 628 nLOE_TXCHFTIONS 1) Daid: D105 sepactad 4 bytes, esq iie ii bytes, Skipping Daid (-214748583), RagisterMag.cpp line 658 (class CRegisterFomDeviceMag), Handled: RagisterMag.cpp line 270 Vilkosption.cpp 628 nLOE_TXCHFTIONS 1) Daid: D105 sepactad 4 bytes, esq iie ii bytes, Skipping Daid (-214748583), RagisterMag.cpp line 658 (class CRegisterFomDeviceMag), Handled: RagisterMag.cpp line 270 Vilkosption.cpp 628 nLOE_TXCHFTIONS 1) Daid: D105 sepactad 4 bytes, esq iie ii bytes, Skipping Daid (-214748583), RagisterMag.cpp line 658 (class CRegisterFomDeviceMag), Handled: RagisterMag.cpp line 270 Vilkosption.cpp 628 nLOE_TXCHFTIONS 1) Daid: D105 sepactad 4 bytes, esq iie ii bytes, Skipping Daid (-214748583), RagisterMag.cpp line 658 (class CRegisterFomDeviceMag), Handled: RagisterMag.cpp line 270 Vilkosption.cpp 628 nLOE_TXCHFTIONS 1) Daid: D105 sepactad 4 bytes, esq iie ii bytes, Skipping Daid (-214748583), RagisterMag.cpp line 658 (class CRegisterFomDeviceMag), Handled: RagisterMag.cpp
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4/1/2017 10:34:25.165 13684 4/1/2017 10:34:25.161 13684	I EXCEPTION (V) HILLER_Y:TF F TankbarsW() I EXCEPTION (V) HILLER_Y:TF F TankbarsW()	1) Daid: D10 sepected 4 bytes, esg size is 1 bytes, Skipping Daid (-214748583), RegisterNeg.cpp line 58 (class CRegisterFonDeviceNag), Handled: RegisterNeg.cpp line 270 Vilkosption.cpp 632 nLO2_EXCEPTIONS 1): Daid: D103 sepected 4 bytes, esg size is 1 bytes, Skipping Daid (-214748583), RegisterNeg.cpp line 658 (class CRegisterFonDeviceNag), Handled: RegisterNeg.cpp line 270 Vilkosption.cpp 632 nLO2_EXCEPTIONS 1): Daid: D103 sepected 4 bytes, esg size is 1 bytes, Skipping Daid (-2147485783), RegisterNeg.cpp line 658 (class CRegisterFonDeviceNag), Handled: RegisterNeg.cpp line 270 Vilkosption.cpp 632 nLO2_EXCEPTIONS 1): Daid: D104 sepected 4 bytes, esg size is 1 bytes, Skipping Daid (-2147485783), RegisterNeg.cpp line 658 (class CRegisterFonDeviceNag), Handled: RegisterNeg.cpp line 270 Vilkosption.cpp 632 nLO2_EXCEPTIONS 1): Daid: D105 sepected 4 bytes, esg size is 1 bytes, Skipping Daid (-214748578), RegisterNeg.cpp line 658 (class CRegisterFonDeviceNag), Handled: RegisterNeg.cpp line 270 Vilkosption.cpp 632 nLO2_EXCEPTIONS 1): Daid: D105 sepected 4 bytes, esg size is 1 bytes, Skipping Daid (-214748578), RegisterNeg.cpp line 658 (class CRegisterFonDeviceNag), Handled: RegisterNeg.cpp line 270 Vilkosption.cpp 632 nLO2_EXCEPTIONS 1): Daid: D105 sepected 4 bytes, esg size is 1 bytes, Skipping Daid (-214748578), RegisterNeg.cpp line 658 (class CRegisterFonDeviceNag), Handled: RegisterNeg.cpp line 270 Vilkosption.cpp 632 nLO2_EXCEPTIONS 1): Daid: D105 sepected 4 bytes, esg size is 1 bytes, Skipping Daid (-214748578), RegisterNeg.cpp line 658 (class CRegisterFonDeviceNag), Handled: RegisterNeg.cpp line 270 Vilkosption.cpp 632 nLO2_EXCEPTIONS 1): Daid: D105 sepected 4 bytes, esg size is 1 bytes, Skipping Daid (-214748578), RegisterNeg.cpp line 658 (class CRegisterFonDeviceNag), Handled: RegisterNeg.cpp line 270 Vilkosption.cpp 632 nLO2_EXCEPTIONS 1): Daid: D105 sepected 4 bytes, esg size is 1 bytes, Skipping Daid (-214748578), RegisterNeg.cpp line 658 (class CReg
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4//2017 10:34:25.165 3864 4//2017 10:34:25.161 3664 4//2017 10:34:25.161 3664	I EXCEPTION (V) HILLER_VIT F TanktasW() EXCEPTION (V) HILLER_VIT F TanktasW()	<pre>1. Daid: D103 expected 4 bytes, reg size is 1 bytes, Skipping Daid (-214748383), RegisterMeg.cop line 58 (class CRegisterFromDeviceMag), Handled: RegisterMeg.cop line 270 Vilkoogtion.cop 628 nLOC_EXCEPTIONS 1: Daid: D103 expected 4 bytes, reg size is 1 bytes, Skipping Daid (-214748383), RegisterMeg.cop line 58 (class CRegisterFromDeviceMag), Handled: RegisterMeg.cop line 270 Vilkoogtion.cop 628 nLOC_EXCEPTIONS 1: Daid: D103 expected 4 bytes, reg size is 1 bytes, Skipping Daid (-214748383), RegisterMeg.cop line 58 (class CRegisterFromDeviceMag), Handled: RegisterMeg.cop line 270 Vilkoogtion.cop 628 nLOC_EXCEPTIONS 1: Daid: D103 expected 4 bytes, reg size is 1 bytes, Skipping Daid (-214748383), RegisterMeg.cop line 58 (class CRegisterFromDeviceMag), Handled: RegisterMeg.cop line 270 Vilkoogtion.cop 628 nLOC_EXCEPTIONS 1: Daid: D105 expected 4 bytes, reg size is 1 bytes, Skipping Daid (-214748383), RegisterMeg.cop line 658 (class CRegisterFromDeviceMag), Handled: RegisterMeg.cop line 270 Vilkoogtion.cop 628 nLOC_EXCEPTIONS 1: Daid: D105 expected 4 bytes, reg size is 1 bytes, Skipping Daid (-214748383), RegisterMeg.cop line 658 (class CRegisterFromDeviceMag), Handled: RegisterMeg.cop line 270 Vilkoogtion.cop 628 nLOC_EXCEPTIONS 1: Daid: D105 expected 4 bytes, reg size is 1 bytes, Skipping Daid (-214748383), RegisterMeg.cop line 658 (class CRegisterFromDeviceMag), Handled: RegisterMeg.cop line 270 Vilkoogtion.cop 628 nLOC_EXCEPTIONS 1: Daid: D105 expected 4 bytes, reg size is 1 bytes, Skipping Daid (-214748383), RegisterMeg.cop line 658 (class CRegisterFromDeviceMag), Handled: RegisterMeg.cop line 270 Vilkoogtion.cop 628 nLOC_EXCEPTIONS 1: Daid: D105 expected 4 bytes, reg size is 1 bytes, Skipping Daid (-214748383), RegisterMeg.cop line 658 (class CRegisterFromDeviceMag), Handled: RegisterMeg.cop line 270 Vilkoogtion.cop 628 nLOC_EXCEPTIONS 1: Daid: D110 expected 4 bytes, reg size is 1 bytes, Skipping Daid (-214748383), RegisterMeg.cop line 658 (class CRegis</pre>
4///2017 0:34:25.145 13684 4///2017 0:34:25.145 13684 4///2017 0:34:25.141 13684	IEXCEPTION (") HILLER_Y-TF F TankbardW() IEXCEPTION (") HILLER_Y-TF T TankbardW() IEXCEPTION (") HILLER_Y-TF T TankbardW()	 baid: D10 sepected 4 bytes, esp iie is 1 bytes, Skipping baid (-21474383), RegisterMag.opp line 59 (line CF RegisterFemDericaSeg), Handled: RegisterMag.opp line 201 Vilkesption.cpp 621 nLOE_INCEPTIONS baid: D10 sepected 4 bytes, esp iie is 1 bytes, Skipping baid (-21474383), RegisterMag.opp line 59 (line CF RegisterFemDericaSeg), Handled: RegisterMag.opp line 201 Vilkesption.cpp 621 nLOE_INCEPTIONS baid: D10 sepected 4 bytes, esp iie is 1 bytes, Skipping baid (-21474383), RegisterMag.opp line 59 (line CF RegisterFemDericaSeg), Handled: RegisterMag.opp line 201 Vilkesption.cpp 621 nLOE_INCEPTIONS baid: D10 sepected 4 bytes, esp iie is 1 bytes, Skipping baid (-21474383), RegisterMag.opp line 59 (line CF RegisterFemDericaSeg), Handled: RegisterMag.opp line 201 Vilkesption.cpp 621 nLOE_INCEPTIONS baid: D10 sepected 4 bytes, esp iie is 1 bytes, Skipping baid (-21474383), RegisterMag.opp line 69 (line CF RegisterFemDericaSeg), Handled: RegisterMag.opp line 201 Vilkesption.cpp 621 nLOE_INCEPTIONS baid: D10 sepected 4 bytes, esp iie is 1 bytes, Skipping baid (-21474383), RegisterMag.opp line 69 (lase CF RegisterFemDericaSeg), Handled: RegisterMag.opp line 201 Vilkesption.cpp 621 nLOE_INCEPTIONS baid: D10 sepected 4 bytes, esp iie i 1 bytes, Skipping baid (-21474383), RegisterMag.opp line 68 (clase CF RegisterFemDericaSeg), Handled: RegisterMag.opp line 201 Vilkesption.cpp 621 nLOE_INCEPTIONS baid: D10 sepected 4 bytes, esp iie i 1 bytes, Skipping baid (-21474383), RegisterMag.opp line 63 (clase CF RegisterFemDericaSeg), Handled: RegisterMag.opp line 201 Vilkesption.cpp 621 nLOE_INCEPTIONS baid: D10 sepected 4 bytes, esp iie i 1 bytes, Skipping baid (-21474383), RegisterMag.opp line 63 (clase CRegisterFremDericaSeg), Handled: RegisterMag.opp line 201 Vilkesption.cpp 621 nLOE_INCEPTIONS baid: D10 sepected 4 bytes, esp iie i 1 bytes, Skippin
4//2017 10:34:25.165 33654 4//2017 10:34:25.161 3664 4//2017 10:34:25.161 3664	EXCEPTION (V) HILLER_VIT F TankbarsW() EXCEPTION (V) HILLER_VIT F TankbarsW()	<pre>1) Daid: DD2 expected 4 bytes, reg lies 1 bytes, Skipping Daid (=21474858), RegisterMed, op line 58 (class CRegisterFomDericMed), Handled, RegisterMed, op line 270 Vilkeeption.cpp 620 nLOC_EXCEPTIONS 1) Daid: DD2 expected 4 bytes, reg lies 1 bytes, Skipping Daid (=21474858), RegisterMed, pp line 58 (class CRegisterFomDericMed), Handled, RegisterMed, op line 270 Vilkeeption.cpp 620 nLOC_EXCEPTIONS 1) Daid: DD2 expected 4 bytes, reg lies 1 bytes, Skipping Daid (=21474858), RegisterMed, pp line 270 Vilkeeption.cpp 620 nLOC_EXCEPTIONS 1) Daid: DD2 expected 4 bytes, reg lies 1 bytes, Skipping Daid (=21474858), RegisterMed, pp line 650 (class CRegisterFomDericMed), Handled, RegisterMed, opp line 270 Vilkeeption.cpp 620 nLOC_EXCEPTIONS 1) Daid: DD2 expected 4 bytes, reg lies 1 bytes, Skipping Daid (=21474858), RegisterMed, pp line 650 (class CRegisterFomDericMed), Handled, RegisterMed, opp line 270 Vilkeeption.cpp 620 nLOC_EXCEPTIONS 1) Daid: DD2 expected 4 bytes, reg lies 1 bytes, Skipping Daid (=21474858), RegisterMed, pp line 650 (class CRegisterFomDericMed), Handled, RegisterMed, opp line 270 Vilkeeption.cpp 620 nLOC_EXCEPTIONS 1) Daid: DD3 expected 4 bytes, reg lies 1 bytes, Skipping Daid (=21474858), RegisterMed, opp line 650 (class CRegisterFomDericMed), Handled, RegisterMed, opp line 270 Vilkeeption.cpp 620 nLOC_EXCEPTIONS 1) Daid: DD3 expected 4 bytes, reg lies 1 bytes, Skipping Daid (=21474858), RegisterMed, opp line 650 (class CRegisterFomDericMed), Handled, RegisterMed, opp line 270 Vilkeeption.cpp 620 nLOC_EXCEPTIONS 1) Daid: DD3 expected 4 bytes, reg lies 1 bytes, Skipping Daid (=21474858), RegisterMed, opp line 650 (class CRegisterFomDericMed), Handled, RegisterMed, opp line 270 Vilkeeption.cpp 621 nLOC_EXCEPTIONS 1) Daid: DD1 expected 4 bytes, reg lies 1 bytes, Skipping Daid (=21474858), RegisterMed, opp line 650 (class CRegisterFomDericMed), Randled, RegisterMed, opp line 270 Vilkeeption.cpp 628 nLOC_EXCEPTIONS 1) Daid: DD1 exp</pre>
4/1/2017 10:34:25.165 13654 4/1/2017 10:34:25.101 1364 4/1/2017 10:34:25.101 1364	I EXCEPTION (V) HILLER_Y:TF F TankbardW() EXCEPTION (V) HILLER_Y:TF F TankbardW() I EXCEPTION (V) WILLER_Y:TF F TankbardW() I EXCEPTION (V) TJ_12_R:TF F TankbardW() I I I I I I I I I I I I I I I I I I I	 baid: D102 expected 4 bytes, esq iie is 1 bytes, Skipping Daid (-21474858), RagisterMag.cpp iin 658 (class CRegisterFomDericaNag), Handled: RagisterMag.cpp iin 201 Vilkeception.cpp 628 nLOE_TXCHFTIONS baid: D104 expected 4 bytes, esq iie is 1 bytes, Skipping Daid (-21474858), RagisterMag.cpp iine 658 (class CRegisterFomDericaNag), Handled: RagisterMag.cpp iine 201 Vilkeception.cpp 628 nLOE_TXCHFTIONS baid: D105 expected 4 bytes, esq iie is 1 bytes, Skipping Daid (-21474858), RagisterMag.cpp iine 658 (class CRegisterFomDericaNag), Handled: RagisterMag.cpp iine 201 Vilkeception.cpp 628 nLOE_TXCHFTIONS baid: D105 expected 4 bytes, esq iie is 1 bytes, Skipping Daid (-21474858), RagisterMag.cpp iine 658 (class CRegisterFomDericaNag), Randled: RegisterMag.cpp iine 270 Vilkeception.cpp 628 nLOE_TXCHFTIONS baid: D105 expected 4 bytes, esq iie is 1 bytes, Skipping Daid (-21474858), RagisterMag.cpp iine 658 (class CRegisterFomDericaNag), Randled: RegisterMag.cpp iine 270 Vilkeception.cpp 628 nLOE_TXCHFTIONS baid: D105 expected 4 bytes, esq iie is 1 bytes, Skipping Daid (-21474858), RagisterMag.cpp iine 658 (class CRegisterFomDericaNag), Randled: RegisterMag.cpp iine 270 Vilkeception.cpp 628 nLOE_TXCHFTIONS baid: D105 expected 4 bytes, esq iie is 1 bytes, Skipping Daid (-21474858), RagisterMag.cpp iine 658 (class CRegisterFomDericaNag), Randled: RegisterMag.cpp iine 270 Vilkeception.cpp 628 nLOE_TXCHFTIONS baid: D105 expected 4 bytes, esq iie is 1 bytes, Skipping Daid (-21474858), RagisterMag.cpp iine 658 (class CRegisterFomDericaNag), Randled: RegisterMag.cpp iine 270 Vilkeception.cpp 628 nLOE_TXCHFTIONS baid: D105 expected 4 bytes, esq iie is 1 bytes, Skipping Daid (-21474858), RagisterMag.cpp iine 658 (class CRegisterFomDericaNag), Randled: RegisterMag.cpp iine 270 Vilkeception.cpp 628 nLOE_TXCHFTIONS baid: D105 expected 4 bytes, esq iie
4//2017 0.034/25.145 3864 4//2017 0.034/25.141 3664 4//2017 0.034/25.141 3664	IXCEFION (Y). HILLER, Y.T.F FankbarsW (). IXCEFION (Y). UTLER, Y.T.F FankbarsW (). IX	<pre>1 Datid DD2 expected 4 bytes, reg lise is 1 bytes, Skipping Deid (~21474838), RegisterMes, rep line 58 (class CRegisterFomDericMes), Handled RegisterMes, rep line 20 Vilkeegtion.cpp 63 inCo_EXCEPTIONS 1 Datid DD2 expected 4 bytes, reg lise is 1 bytes, Skipping Deid (~21474838), RegisterMes, rep line 58 (class CRegisterFomDericMes), Handled RegisterMes, rep line 20 Vilkeegtion.cpp 63 inCo_EXCEPTIONS 1 Datid DD2 expected 4 bytes, reg lise is 1 bytes, Skipping Deid (~21474838), RegisterMes, rep line 58 (class CRegisterFomDericMes), Handled RegisterMes, rep line 20 Vilkeegtion.cpp 63 inCo_EXCEPTIONS 1 Datid DD2 expected 4 bytes, reg lise is 1 bytes, Skipping Deid (~21474838), RegisterMes, rep line 58 (class CRegisterFomDericMes), Handled RegisterMes, rep line 20 Vilkeegtion.cpp 63 inCo_EXCEPTIONS 1 Datid DD2 expected 4 bytes, reg lise is 1 bytes, Skipping Deid (~214748383), RegisterMes, rep line 58 (class CRegisterFomDericMes), Handled RegisterMes, rep line 20 Vilkeegtion.cpp 63 inCo_EXCEPTIONS 1 Datid DD2 expected 4 bytes, reg lise is 1 bytes, Skipping Deid (~214748383), RegisterMes, rep line 58 (class CRegisterFomDericMes), Handled RegisterMes, rep line 20 Vilkeegtion.cpp 63 inCo_EXCEPTIONS 1 Datid DD2 expected 4 bytes, reg lise is 1 bytes, Skipping Deid (~214748383), RegisterMes, rep line 58 (class CRegisterFomDericMes), Handled RegisterMes, rep line 20 Vilkeegtion.cpp 63 inCo_EXCEPTIONS 1 Datid DD3 expected 4 bytes, reg lise is 1 bytes, Skipping Deid (~214748383), RegisterMes, rep line 65 (class CRegisterFomDericMes), Handled RegisterMes, rep line 20 Vilkeegtion.cpp 63 inCo_EXCEPTIONS 2 Datid DD1 expected 4 bytes, reg lise is 1 bytes, Skipping Deid (~214748383), RegisterMes, rep line 65 (class CRegisterFomDericMes), Handled RegisterMes, rep line 20 Vilkeegtion.cpp 63 inCo_EXCEPTIONS 2 Datid DD1 expected 4 bytes, reg lise is 1 bytes, Skipping Deid (~21474838), RegisterMes, rep line 65 (class CRegisterFomDericMes), Handled RegisterMes, rep line 20 Vilke</pre>
$\begin{array}{c} 4/1/2017 0.0342.52.165 1.3664 \\ 4/1/2017 0.0342.5.161 1.3664 \\ 4/1/2017 0.0342.5.161 1.3664 \\ 4/1/2017 0.0342.5.161 1.3664 \\ 4/1/2017 0.0342.5.161 1.3664 \\ 4/1/2017 0.0342.5.161 1.3664 \\ 4/1/2017 0.0342.5.161 1.3664 \\ 4/1/2017 0.0342.5.161 1.3664 \\ 4/1/2017 0.0342.5.161 1.3664 \\ 4/1/2017 0.0342.5.161 1.3664 \\ 4/1/2017 0.0342.5.161 1.3664 \\ 4/1/2017 0.0342.5.161 1.3664 \\ 4/1/2017 0.0342.5.161 1.3664 \\ 4/1/2017 0.0342.5.161 1.3664 \\ 4/1/2017 0.0342.5.161 1.3664 \\ 4/1/2017 0.0342.5.161 1.3664 \\ 4/1/2017 0.0342.5.121 1.3664 \\ 4/1/2017 0.0342.5.121 1.3664 \\ 4/1/2017 0.0342.5.721 1.2768 \\ 4/1/2017 0.0342.5.727 1.2768 \\ 4/1/2017 0.0342.5.727 1.2768 \\ 4/1/2017 0.0342.5.771 1.2768 \\ 4/1/2017 0.0342.5.771 1.2768 \\ 4/1/2017 0.0342.5.771 1.2768 \\ 4/1/2017 0.0342.5.771 1.2768 \\ 4/1/2017 0.0342.5.771 1.2768 \\ 4/1/2017 0.0342.5.771 1.2768 \\ 4/1/2017 0.0342.5.771 1.2768 \\ 4/1/2017 0.0342.5.771 1.2768 \\ 4/1/2017 0.0342.5.771 1.2768 \\ 4/1/2017 0.0342.5.771 1.2768 \\ 4/1/2017 0.0342.5.771 1.2768 \\ 4/1/2017 0.0342.5.771 1.2768 \\ 4/1/2017 0.0342.5.771 1.2768 \\ 4/1/2017 0.0342.5.771 1.2768 \\ 4/1/2017 0.0342.5.771 1.2768 \\ 4/1/2017 0.0342.5.771 1.2768 \\ 4/1/2017 0.0342.5.771 1.2768 \\ 4/1/2017 0.0342.5.771 0.0342.5.771 0.0342.5.771 0.0342.5.778 \\ 4/1/2017 0.0342.5.771 0.0342.5.77$	INCEPTION (V). HILLER_Y.T.F.F.Tankbar4W (d). INCEPTION (V). ULLER_Y.T.F.F.Tankbar4W (d). INCEPTION (V). UT_4.2.A.T.F.F.Tankbar4W (d). INCEPTION (V). UT_4.2.A.T.F.F.Tankbar4W (d). INCEPTIO	 baid: D10 sepected 4 bytes, eq iie i 1 bytes, Skipping Daid (-21474838), RgitzenNag.cpp line 58 (class CRgitzerFemDericaNg), Handled: RgitzenNag.cpp line 270 Vilkesption.cpp 628 nLO_EXCEPTIONS baid: D10 sepected 4 bytes, eq iie i 1 bytes, Skipping Daid (-21474838), RgitzenNag.cpp line 658 (class CRgitzerFemDericaNg), Handled: RgitzenNag.cpp line 270 Vilkesption.cpp 628 nLO_EXCEPTIONS baid: D10 sepected 4 bytes, eq iie i 1 bytes, Skipping Daid (-21474838), RgitzenNag.cpp line 658 (class CRgitzerFemDericaNg), Handled: RgitzenNag.cpp line 270 Vilkesption.cpp 628 nLO_EXCEPTIONS baid: D10 sepected 4 bytes, eq iie i 1 bytes, Skipping Daid (-214748358), RgitzenNag.cpp line 658 (class CRgitzerFemDericaNg), Randled: RgitzenNag.cpp line 270 Vilkesption.cpp 628 nLO_EXCEPTIONS baid: D10 sepected 4 bytes, eq iie i 1 bytes, Skipping Daid (-214748358), AgitzenNag.cpp line 658 (class CRgitzerFemDericaNg), Randled: RgitzenNag.cpp line 270 Vilkesption.cpp 628 nLO_EXCEPTIONS baid: D10 sepected 4 bytes, eq iie i 1 bytes, Skipping Daid (-214748358), AgitzenNag.cpp line 658 (class CRgitzerFemDericaNg), Randled: RgitzenNag.cpp line 270 Vilkesption.cpp 628 nLO_EXCEPTIONS baid: D10 sepected 4 bytes, eq iie i 1 bytes, Skipping Daid (-214748358), AgitzenNag.cpp line 658 (class CRgitzerFemDericaNg), Randled: RgitzenNag.cpp line 270 Vilkesption.cpp 628 nLO_EXCEPTIONS baid: D10 sepected 4 bytes, eq iie i 1 bytes, Skipping Daid (-214748358), RgitzenNag.cpp line 658 (class CRgitzerFemDericaNg), Randled: RgitzenNag.cpp line 270 Vilkesption.cpp 628 nLO_EXCEPTIONS baid: D10 sepected 4 bytes, eq iie i 1 bytes, Skipping Daid (-214748358), RgitzenNag.cpp line 658 (class CRgitzerFemDericaNg), Randled: RgitzenNag.cpp line 270 Vilkesption.cpp 628 nLO_EXCEPTIONS baid: D11 sepected 4 bytes, eq iie i 1 bytes, Skipping Daid (-214748358), RgitzenNag.cpp line 658 (class CRgitz
4//2017 0.034/25.145 13654 4//2017 0.034/25.141 13654 4//2017 10.34/25.141 13654	$ \begin{array}{l} \texttt{IXCEPTION (Y)} & \texttt{HILLER, Y:T} $ $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{HILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{HILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{HILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{HILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{HILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{HILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{HILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{HILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{HILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{HILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{HILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{HILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{HILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{HILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{UILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{UILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{UILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{UILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{UILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{UILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{UILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{UILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{UILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{UILLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{UULLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{UULLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{UULLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{UULLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{UULLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{UULLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION (Y)} & \texttt{UULLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{UULEPTION (Y)} & \texttt{UULLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{UULEPTION (Y)} & \texttt{UULLER, Y:T} $ T = \texttt{TankbarsW}(0) \\ \texttt{UULEPTION (Y)} & \texttt{UULLER, Y:T} $ T = \texttt{UULEPTION (Y)} \\ UUL$	<pre>1 Datid DD2 expected 4 bytes, reg lise is 1 bytes, Skipping Datid (~214748383), RegisterMay, opp line 58 (class CRegisterFomDevicaMay), Banded: RegisterMay, opp line 270 Vilk-ception.cpp 632 nLO2_EXCEPTIONS 1 Datid DD2 expected 4 bytes, reg lise is 1 bytes, Skipping Datid (~214748383), RegisterMay, opp line 58 (class CRegisterFomDevicaMay), Handled: RegisterMay, opp line 270 Vilk-ception.cpp 632 nLO2_EXCEPTIONS 1 Datid DD2 expected 4 bytes, reg lise is 1 bytes, Skipping Datid (~214748383), RegisterMay, opp line 58 (class CRegisterFomDevicaMay), Handled: RegisterMay, opp line 270 Vilk-ception.cpp 632 nLO2_EXCEPTIONS 1 Datid DD2 expected 4 bytes, reg lise is 1 bytes, Skipping Datid (~214748383), RegisterMay, opp line 58 (class CRegisterFomDevicaMay), Handled: RegisterMay, opp line 270 Vilk-ception.cpp 632 nLO2_EXCEPTIONS 1 Datid DD2 expected 4 bytes, reg lise is 1 bytes, Skipping Datid (~214748383), RegisterMay, opp line 58 (class CRegisterFomDevicaMay), Handled: RegisterMay, opp line 270 Vilk-ception.cpp 632 nLO2_EXCEPTIONS 1 Datid DD2 expected 4 bytes, reg lise is 1 bytes, Skipping Datid (~214748383), RegisterMay, opp line 58 (class CRegisterFomDevicaMay), Handled: RegisterMay, opp line 270 Vilk-ception.cpp 632 nLO2_EXCEPTIONS 1 Datid DD2 expected 4 bytes, reg lise is 1 bytes, Skipping Datid (~214748383), RegisterMay, opp line 59 (class CRegisterFomDevicaMay), Handled: RegisterMay, opp line 270 Vilk-ception.cpp 632 nLO2_EXCEPTIONS 1 Datid DD3 expected 4 bytes, reg lise is 1 bytes, Skipping Datid (~214748383), RegisterMay, opp line 59 (class CRegisterFomDevicaMay), Handled: RegisterMay, opp line 270 Vilk-ception.cpp 632 nLO2_EXCEPTIONS 1 Datid DD3 expected 4 bytes, reg lise is 1 bytes, Skipping Datid (~214748383), RegisterMay, opp line 65 (class CRegisterFomDevicaMay), Handled: RegisterMay, opp line 270 Vilk-ception.cpp 632 nLO2_EXCEPTIONS 1 Datid DD3 expected 4 bytes, reg lise is 1 bytes, Skipping Datid (~214748383), RegisterMay, opp line 65 (class CReg</pre>
4///2017 0.034/25.145 3864 4///2017 0.034/25.141 3664 4///2017 0.034/25.271 32768 4///2017 0.034/32.277 32768 4///2017 0.034/32.277 32768	I EXCEPTION (V). HILLER_VIT F TankbarsW() EXCEPTION (V). VILLER_VIT F TankbarsW() EXCEPTION (V). VILLER_VIT F TankbarsW() EXCEPTION (V). V, LALAR VIT F TankbarsW() EXCEPTION (V). V,	<pre>in baid: D102 expected 4 bytes, reg iies in bytes, Skipping baid (-21474838), Rajitzenkag.cpp iie 58 (class CRegitterFombericakag), Handled, Ragitzenkag.cpp iie 270 Vilkoegtion.cpp 628 nLO2_EXCEPTIONS in baid: D105 expected 4 bytes, reg iies in bytes, Skipping baid (-21474838), Ragitzenkag.cpp iie 638 (class CRegitterFombericakag), Handled, Ragitzenkag.cpp iie 270 Vilkoegtion.cpp 628 nLO2_EXCEPTIONS in baid: D105 expected 4 bytes, reg iies in bytes, Skipping baid (-21474838), Ragitzenkag.cpp iie 638 (class CRegitterFombericakag), Handled, Ragitzenkag.cpp iie 270 Vilkoegtion.cpp 628 nLO2_EXCEPTIONS in baid: D105 expected 4 bytes, reg iies in bytes, Skipping baid (-214748383), Ragitzenkag.cpp iies 638 (class CRegitterFombericakag), Handled, Ragitzenkag.cpp iies 270 Vilkoegtion.cpp 628 nLO2_EXCEPTIONS in baid: D105 expected 4 bytes, reg iies in bytes, Skipping baid (-214748383), Ragitzenkag.cpp iies 638 (class CRegitterFombericakag), Handled, Ragitzenkag.cpp iies 270 Vilkoegtion.cpp 628 nLO2_EXCEPTIONS in baid: D105 expected 4 bytes, reg iies in bytes, Skipping baid (-214748383), Ragitzenkag.cpp iine 638 (class CRegitterFombericakag), Handled, Ragitzenkag.cpp iie 270 Vilkoegtion.cpp 628 nLO2_EXCEPTIONS in baid: D105 expected 4 bytes, reg iies in bytes, Skipping baid (-214748383), Ragitzenkag.cpp iine 638 (class CRegitterFombericakag), Handled, Ragitzenkag.cpp iie 270 Vilkoegtion.cpp 628 nLO2_EXCEPTIONS in baid: D105 expected 4 bytes, reg iies in bytes, Skipping baid (-214748383), Ragitzenkag.cpp iine 638 (class CRegitterFombericakag), Handled, Ragitzenkag.cpp iie 270 Vilkoegtion.cpp 628 nLO2_EXCEPTIONS in baid: D104 expected 4 bytes, reg iies in bytes, Skipping baid (-214748383), Ragitzenkag.cpp iine 638 (class CRegitterFombericakag), Handled, Ragitzenkag.cpp iie 270 Vilkoegtion.cpp 628 nLO2_EXCEPTIONS in baid: D104 expected 4 bytes, reg iies in bytes, Skipping baid (-214748383), Ragitzenkag.cpp iine 638 (class CRegitterFombericakag), Handled, Ragitzenkag.</pre>
4///2017 0:34:25.145 13684 4///2017 0:34:25.141 1364 4///2017 0:34:25.771 12788 4///2017 0:34:25.771 12788 4///2017 0:34:25.771 12788	INCEPTION (*) HILLER, Y.T.F F Tankbarawi (*) EXCEPTION (*) F, 44, J.A.T.F F Tankbarawi	<pre>1 baid: D12 expects 4 bytes, reg lise 1s 1 bytes, Skipping baid (-21474383), RegisterMag.opp line 58 (class CRegisterFembericasky), Bandled: RegisterMag.opp line 270 Vilk-explicit, cpp 621 no.C PKCPFTONS 1 baid: D12 expects 4 bytes, reg lise 1s 1 bytes, Skipping baid (-21474383), RegisterMag.opp line 58 (class CRegisterFembericasky), Bandled: RegisterMag.opp line 270 Vilk-explicit, cpp 621 no.C PKCPFTONS 1 baid: D12 expects 4 bytes, reg lise 1s 1 bytes, Skipping baid (-21474383), RegisterMag.opp line 58 (class CRegisterFembericasky), Bandled: RegisterMag.opp line 270 Vilk-explicit, cpp 621 no.C PKCPFTONS 1 baid: D12 expects 4 bytes, reg lise 1s 1 bytes, Skipping baid (-214743838), RegisterMag.opp line 59 (class CRegisterFembericasky), Bandled: RegisterMag.opp line 270 Vilk-explicit, cpp 621 no.C PKCPFTONS 1 baid: D12 expects 4 bytes, reg lise 1s 1 bytes, Skipping baid (-214743838), RegisterMag.opp line 59 (class CRegisterFembericasky), Bandled: RegisterMag.opp line 270 Vilk-explicit, cpp 621 no.C PKCPFTONS 1 baid: D12 expects 4 bytes, reg lise 1 s 1 bytes, Skipping baid (-214743838), RegisterMag.opp line 59 (class CRegisterFembericasky), Bandled: RegisterMag.opp line 270 Vilk-explicit, cpp 621 no.C PKCPFTONS 1 baid: D12 expects 4 bytes, reg lise 1 s 1 bytes, Skipping baid (-214743838), RegisterMag.opp line 59 (class CRegisterFembericasky), Bandled: RegisterMag.opp line 270 Vilk-explicit, cpp 621 no.C PKCPFTONS 1 baid: D12 expects 4 bytes, reg lise 1 s 1 bytes, Skipping baid (-214743838), RegisterMag.opp line 59 (class CRegisterFembericasky), Bandled: RegisterMag.opp line 270 Vilk-explicit, cpp 621 no.C PKCPFTONS 1 baid: D12 expects 4 bytes, reg lise 1 s 1 bytes, Skipping baid (-214743838), RegisterMag.opp line 59 (class CRegisterFembericasky), Bandled: RegisterMag.opp line 270 Vilk-explicit, cpp 621 no.C PKCPFTONS 1 baid: D12 expects 4 bytes, reg lise 1 s 1 bytes, Skipping baid (-214743838), RegisterMag.opp line 59 (class CRegisterFembericasky), Bandled</pre>
4//2017 10:34:25.165 33654 4//2017 10:34:25.161 3664 4//2017 10:34:25.171 3766 4//2017 10:34:25.171 3766 4//2017 10:34:25.171 3766 4//2017 10:34:25.771 3766 4//2017 10:34:25.771 3766 4//2017 10:34:25.771 3766 4//2017 10:34:25.771 3766 4//2017 10:34:25.771 3766 4//2017 10:34:25.771 3766	$ \begin{array}{llllllllllllllllllllllllllllllllllll$	<pre>1 Daid: DD2 expected 4 bytes, reg iies in bytes, Skipping Daid (~21474858), RegisterMag.cpp iin 658 (class CRegisterFromDevicaMag), Handled, RegisterMag.cpp iin 201 Vilkcegtion.cpp 628 nLO2_EXCEPTIONS 1 Daid: DD2 expected 4 bytes, reg iies in bytes, Skipping Daid (~21474858), RegisterMag.cpp iin 658 (class CRegisterFromDevicaMag), Handled, RegisterMag.cpp iin 201 Vilkcegtion.cpp 628 nLO2_EXCEPTIONS 2 Daid: DD2 expected 4 bytes, reg iies in bytes, Skipping Daid (~21474858), RegisterMag.cpp iin 658 (class CRegisterFromDevicaMag), Handled, RegisterMag.cpp iin 201 Vilkcegtion.cpp 628 nLO2_EXCEPTIONS 2 Daid: DD2 expected 4 bytes, reg iies in bytes, Skipping Daid (~214748583), RegisterMag.cpp iin 658 (class CRegisterFromDevicaMag), Handled, RegisterMag.cpp iine 201 Vilkcegtion.cpp 628 nLO2_EXCEPTIONS 2 Daid: DD2 expected 4 bytes, reg iies in bytes, Skipping Daid (~214748583), RegisterMag.cpp iine 658 (class CRegisterFromDevicaMag), Handled, RegisterMag.cpp iine 201 Vilkcegtion.cpp 628 nLO2_EXCEPTIONS 2 Daid: DD3 expected 4 bytes, reg iies in bytes, Skipping Daid (~214748583), RegisterMag.cpp iine 658 (class CRegisterFromDevicaMag), Handled, RegisterMag.cpp iine 201 Vilkcegtion.cpp 628 nLO2_EXCEPTIONS 2 Daid: DD3 expected 4 bytes, reg iies in bytes, Skipping Daid (~214748583), RegisterMag.cpp iine 658 (class CRegisterFromDevicaMag), Handled, RegisterMag.cpp iine 201 Vilkcegtion.cpp 628 nLO2_EXCEPTIONS 2 Daid: DD1 expected 4 bytes, reg iies in bytes, Skipping Daid (~214748583), RegisterMag.cpp iine 658 (class CRegisterFromDevicaMag), Handled, RegisterMag.cpp iine 201 Vilkcegtion.cpp 628 nLO2_EXCEPTIONS 2 Daid: DD1 expected 4 bytes, reg iies in bytes, Skipping Daid (~214748583), RegisterMag.cpp iine 658 (class CRegisterFromDevicaMag), Handled, RegisterMag.cpp iine 201 Vilkcegtion.cpp 628 nLO2_EXCEPTIONS 2 Daid: DD16 expected 4 bytes, reg iies in bytes, Skipping Daid (~214748583), RegisterMag.cpp iiine 658 (class CRegisterFromDevicaMag), Bandled, Registe</pre>
$\begin{array}{c} 4/1/2017 0.034:25.165 .3684 \\ 4/1/2017 0.034:25.161 .3684 \\ 4/1/2017 0.034:25.161 .3684 \\ 4/1/2017 0.034:25.161 .3684 \\ 4/1/2017 0.034:25.161 .3684 \\ 4/1/2017 0.034:25.161 .3684 \\ 4/1/2017 0.034:25.161 .3684 \\ 4/1/2017 0.034:25.161 .3684 \\ 4/1/2017 0.034:25.161 .3684 \\ 4/1/2017 0.034:25.161 .3684 \\ 4/1/2017 0.034:25.161 .3684 \\ 4/1/2017 0.034:25.161 .3684 \\ 4/1/2017 0.034:25.161 .3684 \\ 4/1/2017 0.034:25.161 .3684 \\ 4/1/2017 0.034:25.161 .3684 \\ 4/1/2017 0.034:25.161 .3684 \\ 4/1/2017 0.034:25.161 .3684 \\ 4/1/2017 0.034:25.161 .3684 \\ 4/1/2017 0.034:25.161 .3684 \\ 4/1/2017 0.034:25.171 .2786 \\ 4/1/2017 0.034:25.277 .2786 \\ 4/1/2017 0.034:25.277 .2786 \\ 4/1/2017 0.034:25.277 .2786 \\ 4/1/2017 0.034:25.277 .2786 \\ 4/1/2017 0.034:25.277 .2786 \\ 4/1/2017 0.034:25.277 .2786 \\ 4/1/2017 0.034:25.277 .2786 \\ 4/1/2017 0.034:25.277 .2786 \\ 4/1/2017 0.034:25.277 .2786 \\ 4/1/2017 0.034:25.277 .2786 \\ 4/1/2017 0.034:25.277 .2786 \\ 4/1/2017 0.034:25.277 .2786 \\ 4/1/2017 0.034:25.277 .2786 \\ 4/1/2017 0.034:25.277 .2786 \\ 4/1/2017 0.034:25.277 .2786 \\ 4/1/2017 0.034:25.277 .2786 \\ 4/1/2017 0.034:25.277 .2786 \\ 4/1/2017 0.034:25.277 .2786 \\ 4/1/2017 0.034:25.276 .2786 \\ 4/1/2017 0.034:25.276 .2786 \\ 4/1/2017 0.034:25.276 .2786 \\ 4/1/2017 0.034:25.276 .2786 \\ 4/1/2017 0.034:25.276 .2786 \\ 4/1/2017 0.034:25.276 .2786 \\ 4/1/2017 0.034:25.276 .2786 \\ 4/1/2017 0.034:25.276 .2786 \\ 4/1/2017 0.034:25.276 .2786 \\ 4/1/2017 0.034:25.276 .2786 \\ 4/1/2017 0.034:25.276 .2786 \\ 4/1/2017 0.034:25.276 .2786 \\ 4/1/2017 0.034:25.276 .2786 \\ 4/1/2017 0.034:25.276 .2786 \\ 4/1/2017 0.034:25.276 .2786 \\ 4/1/2017 0.034:25.276 .2786 \\ 4/1/2017 0.034:25.276 .2786 \\ 4/1/2017 0.034:25.276 .2786 .2786 .2786 .$	$ \begin{array}{l} \texttt{EXCEPTION (Y)} & \texttt{HILEB_Y:TF} \ F \ \texttt{TankbarsW}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{HILEB_Y:TF} \ F \ \texttt{TankbarsW}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{HILEB_Y:TF} \ F \ \texttt{TankbarsW}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{HILEB_Y:TF} \ F \ \texttt{TankbarsW}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{HILEB_Y:TF} \ F \ \texttt{TankbarsW}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{HILEB_Y:TF} \ F \ \texttt{TankbarsW}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{HILEB_Y:TF} \ F \ \texttt{TankbarsW}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{HILEB_Y:TF} \ F \ \texttt{TankbarsW}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{HILEB_Y:TF} \ F \ \texttt{TankbarsW}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{HILEB_Y:TF} \ F \ \texttt{TankbarsW}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{HILEB_Y:TF} \ F \ \texttt{TankbarsW}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{HILEB_Y:TF} \ F \ \texttt{TankbarsW}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{HILEB_Y:TF} \ F \ \texttt{TankbarsW}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{HILEB_Y:TF} \ F \ \texttt{TankbarsW}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{UHLEB_Y:TF} \ T \ \texttt{TankbarsW}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{UP} \ \texttt{L} \ \texttt{L} \ \texttt{L} \ \texttt{T} \ \texttt{TankbarsW}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{UP} \ \texttt{L} \ \texttt{L} \ \texttt{L} \ \texttt{T} \ \texttt{TankbarsW}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{UP} \ \texttt{L} \ \texttt{L} \ \texttt{L} \ \texttt{T} \ \texttt{Tankbars}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{UP} \ \texttt{L} \ \texttt{L} \ \texttt{L} \ \texttt{T} \ \texttt{Tankbars}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{UP} \ \texttt{L} \ \texttt{L} \ \texttt{L} \ \texttt{T} \ \texttt{Tankbars}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{UP} \ \texttt{L} \ \texttt{L} \ \texttt{L} \ \texttt{T} \ \texttt{Tankbars}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{UP} \ \texttt{L} \ \texttt{L} \ \texttt{L} \ \texttt{T} \ \texttt{Tankbars}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{UP} \ \texttt{L} \ \texttt{L} \ \texttt{L} \ \texttt{T} \ \texttt{Tankbars}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{UP} \ \texttt{L} \ \texttt{L} \ \texttt{L} \ \texttt{T} \ \texttt{Tankbars}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{UP} \ \texttt{L} \ \texttt{L} \ \texttt{L} \ \texttt{T} \ \texttt{Tankbars}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{UP} \ \texttt{L} \ \texttt{L} \ \texttt{L} \ \texttt{T} \ \texttt{Tankbars}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{UP} \ \texttt{L} \ \texttt{L} \ \texttt{L} \ \texttt{T} \ \texttt{Tankbars}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{UP} \ \texttt{L} \ \texttt{L} \ \texttt{L} \ \texttt{T} \ \texttt{Tankbars}(0) \\ \texttt{EXCEPTION (Y)} & \texttt{UP} \ \texttt{L} \ \texttt{L} \ \texttt{L} \ \texttt{T} \ \texttt$	<pre>1: baid: D10 expected 4 bytes, reg iies 1: bytes, Skipping baid (-21474383), Ragitzenkag, opp iine 59 (liss CRegitzer-Fembericakag), Handlei Ragitzenkag, opp iine 270 Vilkeegtion.cpp 631 mLoC_EXCEPTIONS 1: baid: D10 expected 4 bytes, reg iies 1: bytes, Skipping baid (-21474383), Ragitzenkag.opp iine 59 (liss CRegitzer-Fembericakag), Handlei Ragitzenkag, opp iine 270 Vilkeegtion.cpp 631 mLoC_EXCEPTIONS 1: baid: D10 expected 4 bytes, reg iies 1: bytes, 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1: bytes, Skipping baid (-214743833), Ragitzenkag.opp iine 65 (liss CRegitzer-Fembericakag), Handlei Ragitzenkag, opp iine 270 Vilkeegtion.cpp 631 mLoC_EXCEPTIONS 1: baid: D11 expected 4 bytes, reg iies 1: bytes, Skipping baid (-214743833), Ragitzenkag.opp iine 653 (liss CRegitzer-Fembericakag), Handlei Ragitzenkag.opp 1 iine 270 Vilkeegtion.cpp 632 mLoC_EXCEPTIONS 1: baid: D12 expected 4 bytes, reg iies 1: bytes, Skipping baid (-214743833), Ragitzenkag.opp 1 iine 653 (liss CRegitzer-Fembericakag), Handlei Regitzenkag.opp 1 i</pre>
4//2017 10:34:25.165 33654 4//2017 10:34:25.161 3654 4//2017 10:34:25.171 3765 4//2017 10:34:25.171 3765	$\begin{split} & \texttt{XXEPTION}(Y) = \texttt{WILLER_YTF} F TankbarsW(0) \\ & XXEPTION$	<pre>1. Daid: D12 expects 4 bytes, reg iiis 1 bytes, Skipping Deid (-214748383), RegisterMag.op line 658 (class CRegisterFomDevicaMag), Handled, RegisterMag.op line 201 Vilkeegtion.opp 632 nLO2_EXCEPTIONS 1. Daid: D12 expects 4 bytes, reg iiis 1 bytes, Skipping Deid (-214748383), RegisterMag.op line 658 (class CRegisterFomDevicaMag), Handled, RegisterMag.op line 201 Vilkeegtion.opp 632 nLO2_EXCEPTIONS 1. Daid: D12 expects 4 bytes, reg iiis 1 bytes, Skipping Deid (-214748383), RegisterMag.op line 658 (class CRegisterFomDevicaMag), Handled, RegisterMag.op line 201 Vilkeegtion.opp 632 nLO2_EXCEPTIONS 1. Daid: D126 expects 4 bytes, reg iiis 1 bytes, Skipping Deid (-214748383), RegisterMag.op line 658 (class CRegisterFomDevicaMag), Handled, RegisterMag.opp line 201 Vilkeegtion.opp 632 nLO2_EXCEPTIONS 1. Daid: D126 expects 4 bytes, reg iiis 1 bytes, Skipping Deid (-214748383), RegisterMag.opp line 658 (class CRegisterFomDevicaMag), Handled, RegisterMag.opp line 201 Vilkeegtion.opp 632 nLO2_EXCEPTIONS 1. Daid: D126 expects 4 bytes, reg iiis 1 bytes, Skipping Deid (-214748383), RegisterMag.opp line 658 (class CRegisterFomDevicaMag), Handled, RegisterMag.opp line 201 Vilkeegtion.opp 632 nLO2_EXCEPTIONS 1. Daid: D126 expects 4 bytes, reg iiis 1 bytes, Skipping Deid (-214748383), RegisterMag.opp line 658 (class CRegisterFomDevicaMag), Handled, RegisterMag.opp line 201 Vilkeegtion.opp 632 nLO2_EXCEPTIONS 1. Daid: D126 expects 4 bytes, reg iiis 1 bytes, Skipping Deid (-214748383), RegisterMag.opp line 658 (class CRegisterFomDevicaMag), Handled, RegisterMag.opp line 201 Vilkeegtion.opp 632 nLO2_EXCEPTIONS 1. Daid: D126 expects 4 bytes, reg iiis 1 bytes, Skipping Deid (-214748383), RegisterMag.opp line 658 (class CRegisterFomDevicaMag), Handled, RegisterMag.opp line 201 Vilkeegtion.opp 632 nLO2_EXCEPTIONS 1. Daid: D126 expects 4 bytes, reg iiis 1 bytes, Skipping Deid (-214748383), RegisterMag.opp line 658 (class CRegisterFomDevicaMag), Handled, RegisterMag.opp line 201 V</pre>
$\begin{array}{c} 4/1/2017 \; 0.0342.5.165 \; 38684 \\ 4/1/2017 \; 0.0342.5.161 \; 38644 \\ 4/1/2017 \; 0.0342.5.161 \; 3864 \\ 4/1/2017 \; 0.0342.5.161 \; 3864 \\ 4/1/2017 \; 0.0342.5.161 \; 3864 \\ 4/1/2017 \; 0.0342.5.161 \; 3864 \\ 4/1/2017 \; 0.0342.5.161 \; 3864 \\ 4/1/2017 \; 0.0342.5.161 \; 3864 \\ 4/1/2017 \; 0.0342.5.161 \; 3864 \\ 4/1/2017 \; 0.0342.5.161 \; 3864 \\ 4/1/2017 \; 0.0342.5.161 \; 3864 \\ 4/1/2017 \; 0.0342.5.161 \; 3864 \\ 4/1/2017 \; 0.0342.5.161 \; 3864 \\ 4/1/2017 \; 0.0342.5.161 \; 3864 \\ 4/1/2017 \; 0.0342.5.161 \; 3864 \\ 4/1/2017 \; 0.0342.5.161 \; 3864 \\ 4/1/2017 \; 0.0342.5.161 \; 3864 \\ 4/1/2017 \; 0.0342.5.121 \; 3864 \\ 4/1/2017 \; 0.0342.721 \; 32764 \\ 4/1/2017 \; 0.0342.727 \; 32764 \\ 4/1/2017 \; 0.0342.3.727 \; 32764 \\ 4/1/2017 \; 0.0342.5.727 \; 32764 \\ 4/1/2017 \; 0.0342.5.727 \; 32764 \\ 4/1/2017 \; 0.0342.5.727 \; 32764 \\ 4/1/2017 \; 0.0342.5.727 \; 32764 \\ 4/1/2017 \; 0.0342.5.727 \; 32764 \\ 4/1/2017 \; 0.0342.5.727 \; 32764 \\ 4/1/2017 \; 0.0342.5.727 \; 32764 \\ 4/1/2017 \; 0.0342.5.727 \; 32764 \\ 4/1/2017 \; 0.0342.5.727 \; 32764 \\ 4/1/201$	$ \begin{array}{l} \texttt{IXCEPTION} (V) & \texttt{HILER_V:TF} F \texttt{TankbarsW}(0) \\ \texttt{IXCEPTION} (V) & \texttt{IXIER_V:TF} T \texttt{IXIER_V:TF} T \texttt{IXIER_V:TF} \\ \texttt{IXCEPTION} (V) & \texttt{IXIER_V:TF} T \texttt{IXIER_V:TF} T \texttt{IXIER_V:TF} \\ \texttt{IXCEPTION} (V) & \texttt{IXIER_V:TF} T \texttt{IXIER_V:TF} \texttt{IXIER_V:TF} \\ \texttt{IXCEPTION} (V) & \texttt{IXIER_V:TF} T \texttt{IXIER_V:TF} T \texttt{IXIER_V:TF} \\ \texttt{IXCEPTION} (V) & \texttt{IXIER_V:TF} \texttt{IXIER_V:TF} T \texttt{IXIER_V:TF} \\ \texttt{IXCEPTION} (V) & \texttt{IXIER_V:TF} T \texttt{IXIER_V:TF} T \texttt{IXIER_V:TF} \\ \texttt{IXCEPTION} (V) & \texttt{IXIER_V:TF} \texttt{IXIER_V:TF} T \texttt{IXIER_V:TF} \\ \texttt{IXCEPTION} (V) & \texttt{IXIER_V:TF} IXI$	<pre>i> baid: D10 expected 4 bytes, reg iies is 1 bytes, Skipping baid (-214748383), RegitzerMag.opp line 59 (line CFR) (SegitzerFrombericasky), Handled: RegitzerMag.opp line 201 Vilk-ception.cpp 621 nLO_EXCEPTIONS i> baid: D10 expected 4 bytes, reg iies is 1 bytes, Skipping baid (-214748783), RegitzerMag.opp line 59 (line CFR) (SegitzerFrombericasky), Handled: RegitzerMag.opp line 201 Vilk-ception.cpp 621 nLO_EXCEPTIONS i> baid: D10 expected 4 bytes, reg iies is 1 bytes, Skipping baid (-214748783), RegitzerMag.opp line 59 (line CFR) (SegitzerFrombericasky), Handled: RegitzerMag.opp line 201 Vilk-ception.cpp 628 nLO_EXCEPTIONS i> baid: D10 expected 4 bytes, reg iies is 1 bytes, Skipping baid (-214748783), RegitzerMag.opp line 63 (lase CRegitzerFrombericasky), Handled: RegitzerMag.opp line 201 Vilk-ception.cpp 628 nLO_EXCEPTIONS i> baid: D10 expected 4 bytes, reg iies is 1 bytes, Skipping baid (-214748783), RegitzerMag.opp line 63 (lase CRegitzerFrombericasky), Handled: RegitzerMag.opp line 201 Vilk-ception.cpp 628 nLO_EXCEPTIONS i> baid: D10 expected 4 bytes, reg iies is 1 bytes, Skipping baid (-214748783), RegitzerMag.opp line 63 (lase CRegitzerFrombericasky), Handled: RegitzerMag.opp line 201 Vilk-ception.cpp 628 nLO_EXCEPTIONS i> baid: D10 expected 4 bytes, reg iies is 1 bytes, Skipping baid (-214748783), RegitzerMag.opp line 63 (lase CRegitzerFrombericasky), Handled: RegitzerMag.opp line 201 Vilk-ception.cpp 628 nLO_EXCEPTIONS i> baid: D10 expected 4 bytes, reg iies is 1 bytes, Skipping baid (-214748783), RegitzerMag.opp line 63 (lase CRegitzerFrombericasky), Handled: RegitzerMag.opp line 201 Vilk-ception.cpp 628 nLO_EXCEPTIONS i> baid: D10 expected 4 bytes, reg iies is 1 bytes, Skipping baid (-214748783), RegitzerMag.opp line 63 (lase CRegitzerFrombericasky), Handled: RegitzerMag.opp line 201 Vilk-ception.cpp 628 nLO_EXCEPTIONS i> baid: D10 expected 4 bytes, reg iies is 1 bytes, Skipping baid (-214748783), RegitzerMag.opp line 63 (lase CRegitzerFromberi</pre>
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$\begin{array}{c} 4/1/2017 \ 0.034:25.165 \ 1.3664 \\ 4/1/2017 \ 0.034:25.161 \ 1.3664 \\ 4/1/2017 \ 0.034:25.161 \ 1.3664 \\ 4/1/2017 \ 0.034:25.161 \ 1.3664 \\ 4/1/2017 \ 0.034:25.161 \ 1.3664 \\ 4/1/2017 \ 0.034:25.161 \ 1.3664 \\ 4/1/2017 \ 0.034:25.161 \ 1.3664 \\ 4/1/2017 \ 0.034:25.161 \ 1.3664 \\ 4/1/2017 \ 0.034:25.161 \ 1.3664 \\ 4/1/2017 \ 0.034:25.161 \ 1.3664 \\ 4/1/2017 \ 0.034:25.161 \ 1.3664 \\ 4/1/2017 \ 0.034:25.161 \ 1.3664 \\ 4/1/2017 \ 0.034:25.161 \ 1.3664 \\ 4/1/2017 \ 0.034:25.161 \ 1.3664 \\ 4/1/2017 \ 0.034:25.161 \ 1.3664 \\ 4/1/2017 \ 0.034:25.161 \ 1.3664 \\ 4/1/2017 \ 0.034:25.161 \ 1.3664 \\ 4/1/2017 \ 0.034:25.161 \ 1.3664 \\ 4/1/2017 \ 0.034:25.161 \ 1.3664 \\ 4/1/2017 \ 0.034:25.271 \ 1.2766 \\ 4/1/2017 \ 0.034:25.277 \ 1.2766 \\ 4/1/2017 $	EXCEPTION (V) = HILLER_VIT F Tankbards () EXCEPTION (V) = HIL	<pre>1 Daid: D12 expected 4 bytes, reg iiis i bytes, Skipping Daid (=14743583), RegisterMag.opp line 58 (class CRegisterFomDevicaMag), Handled, RegisterMag.opp line 270 Vilkeegtion.opp 621 nLO2_EXCEPTIONS 2) Daid: D12 expected 4 bytes, reg iiis i bytes, Skipping Daid (=14743583), RegisterMag.opp line 58 (class CRegisterFomDevicaMag), Handled, RegisterMag.opp line 270 Vilkeegtion.opp 621 nLO2_EXCEPTIONS 2) Daid: D12 expected 4 bytes, reg iiis i bytes, Skipping Daid (=14743583), RegisterMag.opp line 58 (class CRegisterFomDevicaMag), Handled, RegisterMag.opp line 270 Vilkeegtion.opp 621 nLO2_EXCEPTIONS 2) Daid: D12 expected 4 bytes, reg iiis i bytes, Skipping Daid (=14743583), RegisterMag.opp line 68 (class CRegisterFomDevicaMag), Handled, RegisterMag.opp line 270 Vilkeegtion.opp 621 nLO2_EXCEPTIONS 2) Daid: D12 expected 4 bytes, reg iiis i bytes, Skipping Daid (=14743583), RegisterMag.opp line 68 (class CRegisterFomDevicaMag), Handled, RegisterMag.opp line 270 Vilkeegtion.opp 621 nLO2_EXCEPTIONS 2) Daid: D12 expected 4 bytes, reg iiis i bytes, Skipping Daid (=14743583), RegisterMag.opp line 68 (class CRegisterFomDevicaMag), Handled, RegisterMag.opp line 270 Vilkeegtion.opp 621 nLO2_EXCEPTIONS 2) Daid: D13 expected 4 bytes, reg iiis i bytes, Skipping Daid (=14743638), RegisterMag.opp line 68 (class CRegisterFomDevicaMag), Handled, RegisterMag.opp line 270 Vilkeegtion.opp 621 nLO2_EXCEPTIONS 2) Daid: D13 expected 4 bytes, reg iiis i bytes, Skipping Daid (=14743638), RegisterMag.opp line 68 (class CRegisterFomDevicaMag), Handled, RegisterMag.opp line 270 Vilkeegtion.opp 621 nLO2_EXCEPTIONS 2) Daid: D112 expected 4 bytes, reg iiis i bytes, Skipping Daid (=14743638), RegisterMag.opp line 68 (class CRegisterFomDevicaMag), Handled, RegisterMag.opp line 70 Vilkeegtion.opp 621 nLO2_EXCEPTIONS 2) Daid: D112 expected 4 bytes, reg iiis i bytes, Skipping Daid (=14743638), RegisterMag.opp line 68 (class CRegisterFomDevicaMag), Handled, RegisterMag.opp line 70 Vil</pre>
4//2017 10:34:25.145 13654 4//2017 10:34:25.141 13654 4//2017 10:34:25.171 1768 4//2017 10:34:25.771 17788 4//2017 10:34:25.771 17788	$ \begin{array}{llllllllllllllllllllllllllllllllllll$	<pre>1 baid: D12 expects 4 bytes, reg iiis 1 bytes, Skipping baid (-214743838), RegitzerMag.opp line 58 (class CRegitzerFombericasky), Handled: RegitzerMag.opp line 270 Vilk-ception.cpp 632 nic0_RXXEPTIONS 1 baid: D12 expects 4 bytes, reg iiis 1 bytes, Skipping baid (-214743838), RegitzerMag.opp line 58 (class CRegitzerFombericasky), Handled: RegitzerMag.opp line 270 Vilk-ception.cpp 632 nic0_RXXEPTIONS 1 baid: D12 expects 4 bytes, reg iiis 1 bytes, Skipping baid (-214743838), RegitzerMag.opp line 59 (class CRegitzerFombericasky), Handled: RegitzerMag.opp line 270 Vilk-ception.cpp 632 nic0_RXXEPTIONS 1 baid: D12 expects 4 bytes, reg iiis 1 bytes, Skipping baid (-214743838), RegitzerMag.opp line 59 (class CRegitzerFombericasky), Handled: RegitzerMag.opp line 270 Vilk-ception.cpp 632 nic0_RXXEPTIONS 1 baid: D12 expects 4 bytes, reg iiis 1 bytes, Skipping baid (-214743838), RegitzerMag.opp line 59 (class CRegitzerFombericasky), Handled: RegitzerMag.opp line 270 Vilk-ception.cpp 632 nic0_RXXEPTIONS 1 baid: D12 expects 4 bytes, reg iiis 1 bytes, Skipping baid (-214743838), RegitzerMag.opp line 59 (class CRegitzerFombericasky), Handled: RegitzerMag.opp line 270 Vilk-ception.cpp 632 nic0_RXXEPTIONS 1 baid: D12 expects 4 bytes, reg iiis 1 bytes, Skipping baid (-214743838), RegitzerMag.opp line 59 (class CRegitzerFombericasky), Handled: RegitzerMag.opp line 270 Vilk-ception.cpp 632 nic0_RXXEPTIONS 1 baid: D12 expects 4 bytes, reg iiis 1 bytes, Skipping baid (-214743838), RegitzerMag.opp line 59 (class CRegitzerFombericasky), Handled: RegitzerMag.opp line 270 Vilk-ception.cpp 632 nic0_RXXEPTIONS 1 baid: D12 expects 4 bytes, reg iiis 1 bytes, Skipping baid (-214743838), RegitzerMag.opp line 659 (class CRegitzerFombericasky), Handled: RegitzerMag.opp line 270 Vilk-ception.cpp 632 nic0_RXEPTIONS 1 baid: D12 expects 4 bytes, reg iiis 1 bytes, Skipping baid (-214743838), RegitzerMag.opp line 659 (class CRegitzerFombericasky), Handled: RegitzerMag.opp line 270 Vilk-ceptio</pre>
4//2017 10:34:25.165 33654 4//2017 10:34:25.161 3664 4//2017 10:34:25.171 3764 4//2017 10:34:25.771 3764 4//2017 10:34:25.777 37764 4//2017 10:34:25.777 37764	$\begin{aligned} & \texttt{XCEPTION}(Y) = \texttt{WILLER_YTT} F Tankbars(G) \\ & $	<pre>1 Daid: D12 expected 4 bytes, reg iies i bytes, Skipping Deid (-214748383), RegisterMag.opp line 658 (class CRegisterFromDevicaMag), Handled, RegisterMag.opp line 270 Vilkeegtion.opp 632 nic0_EXCEPTIONS 1 Daid: D12 expected 4 bytes, reg iies i bytes, Skipping Deid (-214748383), RegisterMag.opp line 658 (class CRegisterFromDevicaMag), Handled, RegisterMag.opp line 270 Vilkeegtion.opp 632 nic0_EXCEPTIONS 1 Daid: D126 expected 4 bytes, reg iies i bytes, Skipping Deid (-214748383), RegisterMag.opp line 658 (class CRegisterFromDevicaMag), Handled, RegisterMag.opp line 270 Vilkeegtion.opp 632 nic0_EXCEPTIONS 1 Daid: D126 expected 4 bytes, reg iies i bytes, Skipping Deid (-214748383), RegisterMag.opp line 658 (class CRegisterFromDevicaMag), Handled, RegisterMag.opp line 270 Vilkeegtion.opp 632 nic0_EXCEPTIONS 1 Daid: D126 expected 4 bytes, reg iies i b bytes, Skipping Deid (-214748383), RegisterMag.opp line 658 (class CRegisterFromDevicaMag), Handled, RegisterMag.opp line 270 Vilkeegtion.opp 632 nic0_EXCEPTIONS 1 Daid: D126 expected 4 bytes, reg iies i b bytes, Skipping Deid (-214748383), RegisterMag.opp line 658 (class CRegisterFromDevicaMag), Handled, RegisterMag.opp line 270 Vilkeegtion.opp 632 nic0_EXCEPTIONS 1 Daid: D126 expected 4 bytes, reg iies i b bytes, Skipping Deid (-214748383), RegisterMag.opp line 658 (class CRegisterFromDevicaMag), Handled, RegisterMag.opp line 270 Vilkeegtion.opp 632 nic0_EXCEPTIONS 1 Daid: D126 expected 4 bytes, reg iies i b bytes, Skipping Deid (-214748383), RegisterMag.opp line 658 (class CRegisterFromDevicaMag), Handled, RegisterMag.opp line 270 Vilkeegtion.opp 632 nic0_EXCEPTIONS 1 Daid: D126 expected 4 bytes, reg iies i b bytes, Skipping Deid (-214748383), RegisterMag.opp line 658 (class CRegisterFromDevicaMag), Handled, RegisterMag.opp line 270 Vilkeegtion.opp 632 nic0_EXCEPTIONS 1 Daid: D126 expected 4 bytes, reg iies i b bytes, Skipping Deid (-214748383), RegisterMag.opp line 658 (class CRegisterFromDevicaMag), Bandled</pre>
$\begin{array}{c} 4/1/2017 \ 0.0342.51.46 \ 1.3664 \\ 4/1/2017 \ 0.0342.51.46 \ 1.3664 \\ 4/1/2017 \ 0.0342.51.46 \ 1.3664 \\ 4/1/2017 \ 0.0342.51.46 \ 1.3664 \\ 4/1/2017 \ 0.0342.51.46 \ 1.3664 \\ 4/1/2017 \ 0.0342.51.46 \ 1.3664 \\ 4/1/2017 \ 0.0342.51.46 \ 1.3664 \\ 4/1/2017 \ 0.0342.51.46 \ 1.3664 \\ 4/1/2017 \ 0.0342.51.46 \ 1.3664 \\ 4/1/2017 \ 0.0342.51.46 \ 1.3664 \\ 4/1/2017 \ 0.0342.51.46 \ 1.3664 \\ 4/1/2017 \ 0.0342.51.41 \ 1.3664 \\ 4/1/2017 \ 0.0342.51.41 \ 1.3664 \\ 4/1/2017 \ 0.0342.51.41 \ 1.3664 \\ 4/1/2017 \ 0.0342.72 \ 1.2768 \\ 4/1/2017 \ 0.0342.72 \ 1.2778 \\ 4/1/2017 \ 0.0342.72 \ 1.2778 \\ 4/1/2017 \ 0.0342.72 \ 1.2778 \\ 4/1/2017 \ 0.0342.72 \ 1.2778 \\ 4/1/2017 \ 0.0342.72 \ 1.2778 \\ 4/1/2017 \ 0.0342.72 \ 1.2778 \\ 4/1/2017 \ 0.0342.72 \ 1.2778 \\ 4/1/2017 \ 0.0342.72 \ 1.2778 \\ 4/1/2017 \ 0.0342.72 \ 1.2788 \\ 4/1/2017 \ 0.0342.72 \ 1.2788 \\ 4/1/2017 \ 0.0342.72 \ 1.2788 \\ 4/1/2017 \ 0.0342.72 \ 1.2788 \\ 4/1/2017 \ 0.0342.72 \ 1.2788 \\ 4/1/2017 \ 0.0342.72 \ 1.2788 \\ 4/1/2017 \ 0.0342.72 \ 1.2788 \\ 4/1/2017 \ 0.0342.72 \ 1.2788 \\ 4/1/2017 \ 0.0342.72 \ 1.2788 \\ 4/1/2017 \ 0.0342.72 \ 1.2788 \\ 4/1/2017 \ 0.0342.72 \ 1.2788 \\ 4/1/2017 \ 0.0342.72 \ 1.2788 \\ 4/1/2017 \ 0.0342.72 \ 1.2788 \\ 4/1/2017 \ 0.0342.72 \ 1.2788 \\ 4/1/2017 \ 0.0342.72 \ 1.2788 \\ 4/1/2017 \ 0.0342.72 \ 1.2788 \\ 4/1/2017 \ 0.0342.72 \ 1.2788 \\ 4/1/2017 \ 0.0342.52 \ 71 \ 1.2788 \\ 4/1/2017 \ 0.0342.52 \ 71 \ 1.2788 \\ 4/1/2017 \ 0.0342.52 \ 71 \ 1.2788 \\ 4/1/2017 \ 0.0342.52 \ 71 \ 1.2788 \\ 4/1/2017 \ 0.0342.52 \ 71 \ 1.2788 \\ 4/1/2017 \ 0.0342.52 \ 71 \ 1.2788 \\ 4/1/2017 \ 0.0342.52 \ 71 \ 1.2788 \\ 4/1/2017 \ 0.0342.52 \ 71 \ 1.2788 \\ 4/1/2017 \ 0.0342.52 \ 71 \ 1.2788 \\ 4/1/2017 \ 0.0342.52 \ 71 \ 1.2788 \\ 4/1/2017 \ 0.0342.52 \ 71 \ 1.2788 \\ 4/1/2017 \ 0.0342.52 \ 71 \ 1.2788 \\ 4/1/2017 \ 0.0342.52 \ 71 \ 1.2788 \\ 4/1/2017 \ 0.0342.52 \ 71 \ 1.2788 \\ 4/1/2017 \ 0.0342.52 \ 71 \ 1.2788 \\ 4/1/2017 \ 0.0342.52 \ 71 \ 1.2788 \\ 4/1/2017 \ 0.0342.52 \ 71 \ 1.2788 \ 71 \ 71 \ 71 \ 71 \ 71 \ 71 \ 71 \$	$ \begin{split} & \texttt{EXCEPTION (Y)} = \texttt{HILLER_YTF} F \texttt{PankbarsW}(0) \\ & \texttt{EXCEPTION (Y)} = \texttt{HILLR_YTF} F \texttt{HILR} + \texttt{HILR} + \texttt{HILR} F \texttt{HILR} + $	<pre>1 baid: D10 expected 4 bytes, eq iiis i 1 bytes, Skipping baid (-214743838), Regitzenkag, opp line 58 (class CRegitzerFembericaskg), Handlet Regitzenkag, opp line 270 Vilkeeqtion.cpp 621 nLO_EXCEPTIONS 1 baid: D10 expected 4 bytes, eq iiis i 1 bytes, Skipping baid (-214743838), Regitzenkag, opp line 58 (class CRegitzerFembericaskg), Handlet Regitzenkag, opp line 270 Vilkeeqtion.cpp 621 nLO_EXCEPTIONS 1 baid: D10 expected 4 bytes, eq iiis i 1 bytes, Skipping baid (-214743838), Regitzenkag, opp line 59 (class CRegitzerFembericaskg), Handlet Regitzenkag, opp line 270 Vilkeeqtion.cpp 621 nLO_EXCEPTIONS 1 baid: D10 expected 4 bytes, eq iiis i 1 bytes, Skipping baid (-214743838), Regitzenkag, opp line 59 (class CRegitzerFembericaskg), Handlet Regitzenkag, opp line 270 Vilkeeqtion.cpp 621 nLO_EXCEPTIONS 1 baid: D10 expected 4 bytes, eq iiis i 1 bytes, Skipping baid (-214743838), Regitzenkag, opp line 59 (class CRegitzerFembericaskg), Handlet Regitzenkag, opp line 270 Vilkeeqtion.cpp 621 nLO_EXCEPTIONS 1 baid: D10 expected 4 bytes, eq iiis i 1 bytes, Skipping baid (-214743838), Regitzenkag, opp line 59 (class CRegitzerFembericaskg), Handlet Regitzenkag, opp line 270 Vilkeeqtion.cpp 621 nLO_EXCEPTIONS 1 baid: D10 expected 4 bytes, eq iiis i 1 bytes, Skipping baid (-214743838), Regitzenkag, opp line 59 (class CRegitzerFembericaskg), Handlet Regitzenkag, opp line 270 Vilkeeqtion.cpp 621 nLO_EXCEPTIONS 1 baid: D10 expected 4 bytes, eq iiis i 1 bytes, Skipping baid (-214743838), Regitzenkag, opp line 69 (class CRegitzerFembericaskg), Handlet Regitzenkag, opp line 270 Vilkeeqtion.cpp 621 nLO_EXCEPTIONS 1 baid: D10 expected 4 bytes, eq iiis 1 1 bytes, Skipping baid (-214743838), Regitzenkag, opp line 69 (class CRegitzerFembericaskg), Handlet Regitzenkag, opp line 270 Vilkeeqtion.cpp 621 nLO_EXCEPTIONS 1 baid: D10 expected 4 bytes, eq iis 1 2 bytes, Skipping baid (-214743838), Regitzenkag, opp line 69 (class CRegitzerFembericaskg), Handlet Regitzenkag, opp line 270 V</pre>
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- New in 8.5.0
 - Extended logging



- New in 8.5.0
 - Extended logging -Logging Keywords-– Opt-in LOGMASK ELS CONTROL EXCEPTIONS LOGMASK FILE CONTROL EXCEPTIONS WARNING # 2 LOGFILE FILE COUNT LOGFILE FILE SIZE 100 100000 LOGFILE LINE COUNT LOGFILE LIMIT MODE LINE LOGFILE MODE EXTENDED

Recommendations and Optimizations

Preparing for Mayhem

Recommendations & Optimizations

Why Optimization?



Recommendations & Optimizations

- Resource bottlenecks
 - Memory
 - CPU
 - Drive I/O
- Demand > availability
 - Decrease demand
 - Increase availability

The journey of a UIS message







- Bottleneck #1 Memory
 - 32-bit process 4GB memory limit
 - 64-bit process 16.8 million terabyte memory limit

- Preventing death by pending comm queue
 - Alarm
 - Virtual memory (SVMVIRT)
 - Pending comm queue (SVMUISPNDC)
 - Failed communications (SYCSSTAT)
 - Optimize
 - Throttle









🖓 Windows Task Manager
File Options View Help
Applications Processes Services Performance Networking Users
CPU Usage History

- Bottleneck #2 CPU
 - Default: 3 threads per response queue
 - Configurable up to 16 threads

- Increase thread count
 - Add incrementally

Increase thread count

12 Threads Configured

🙀 Windows Task Manager							- 🗆 ×
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Bottleneck #3 – Disk I/O



- Bottleneck #3 Disk I/O
 - Disk queue length (SVMADQLC) < 1</p>
- Solutions
 - Reduce read/writes
 - Increase cache
 - Upgrade drive



Protect Your Enterprise from Data Loss and Downtime

Cleaning up after Mayhem

- Backup
- Replication

- Backup pros
 - Protects against file corruption and accidental deletion
 - Automatic Service Recovery (ASR)
 - Simple to implement

- Backup cons
 - Longer downtime
 - May lose recent changes
 - Does not address hardware failure

- Replication pros
 - Changes protected in real-time
 - Significantly reduced downtime
 - Protection against hardware failure

- Replication cons
 - Does not protect against file corruption or accidental deletion
 - Requires separate hardware
 - Implementation is typically more complicated

Recommended Practices

- Backups
 - Maintain multiple versions
 - Keep off production system
 - Verify backup success

Recommended Practices

- Replication
 - Test failover



What about Redundancy?

Redundancy

- Utilizes replication
 - Same pros and cons
 - 8.5.0 On-demand failover
 - 8.5.1 Automatic failover



- Catching Mayhem at the door
 - SVCMON
 - Logging

- Preparing for Mayhem
 - Optimizing the System
 - Identifying bottlenecks

Cleaning up after Mayhem

– Backup + Replication = Recovery Plan



Disaster Prevention How to prepare for a visit from Mayhem



Additional Resources

- CygNet Blog
- CygNet Support
- 2017 Support Projects



Disaster Prevention

Questions?